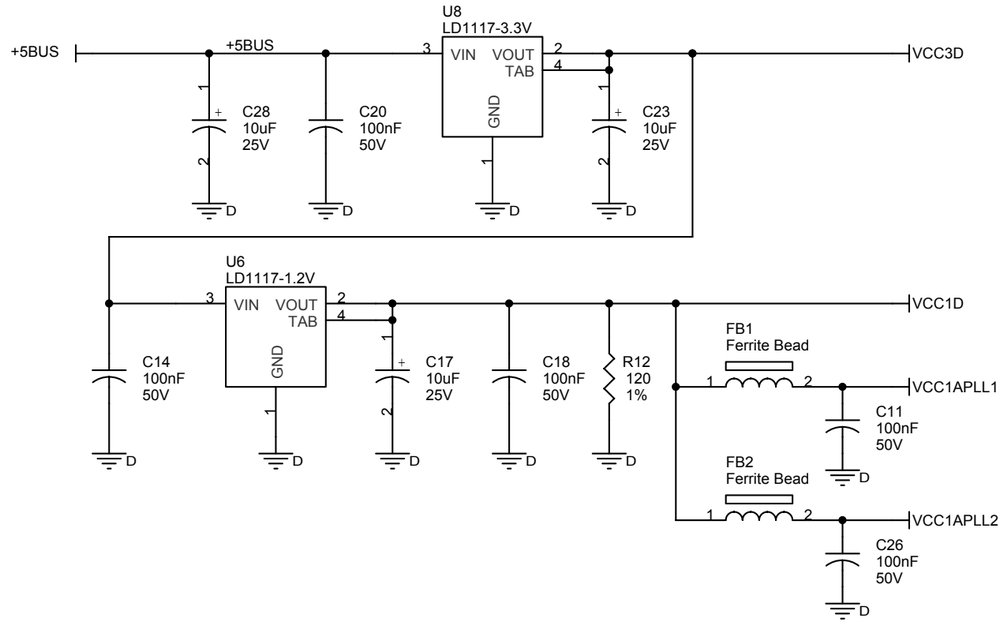
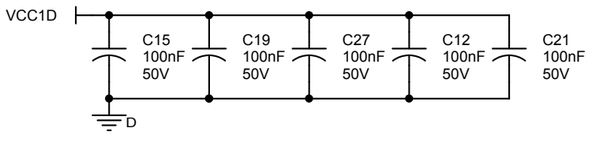
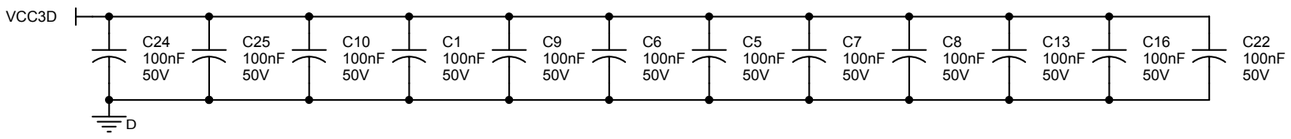
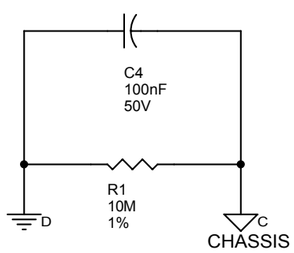


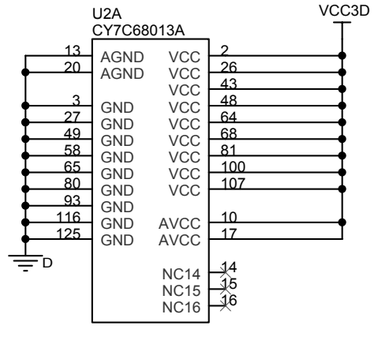


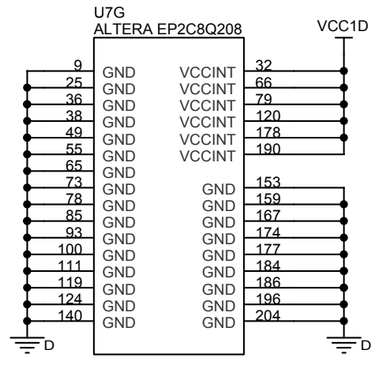


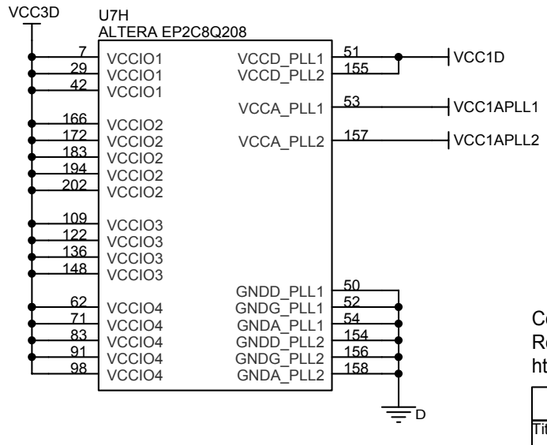
Revision History

Date	Revision	By	Description
22 Aug 2009	XA6	WA2DFI	Reformatted from Lyle's schematic, added properties
26 Aug 2009	XA7	KK7P	Changed connections of D10, D11; Q1, Q2 to 2N7002K (Sheet 5).
19 Sep 2010	A	KK7P	Changed design material license from NCL to OHL.









Title: HPSDR Magister: Bypass and Power		
Size: B	Document Number: 2009071901	Rev: A
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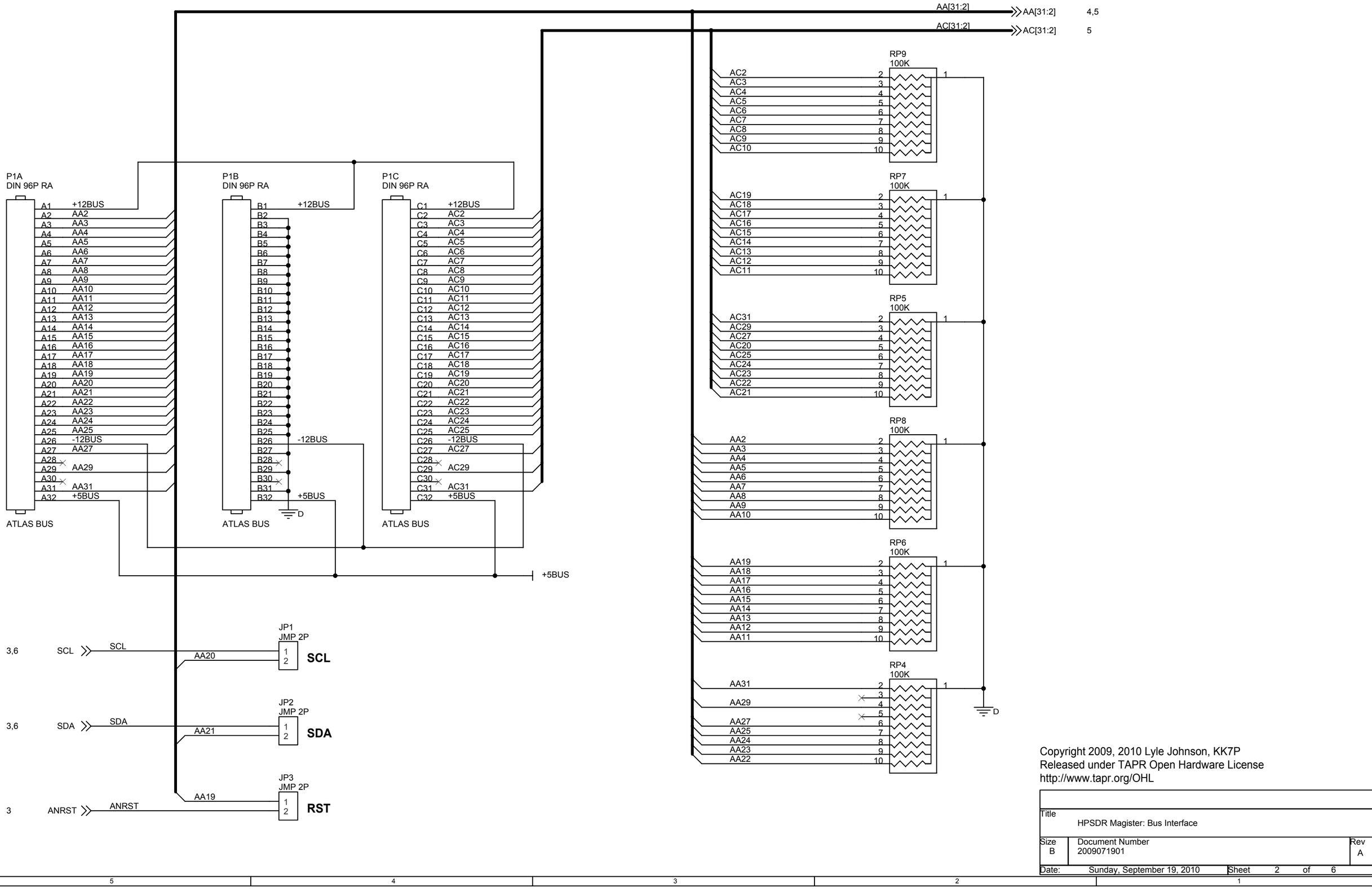
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AA[31:2] 4,5
AC[31:2]
AC[31:2] 5
P1A
DIN 96P RA
P1B
DIN 96P RA
P1C
DIN 96P RA
+12BUS
-12BUS
+5BUS
ATLAS BUS
RP9
100K
RP7
100K
RP5
100K
RP8
100K
RP6
100K
RP4
100K
JP1
JMP 2P
SCL
JP2
JMP 2P
SDA
JP3
JMP 2P
RST
3,6 SCL
3,6 SDA
3 ANRST
AA20
AA21
AA19
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Title
HPSDR Magister: Bus Interface
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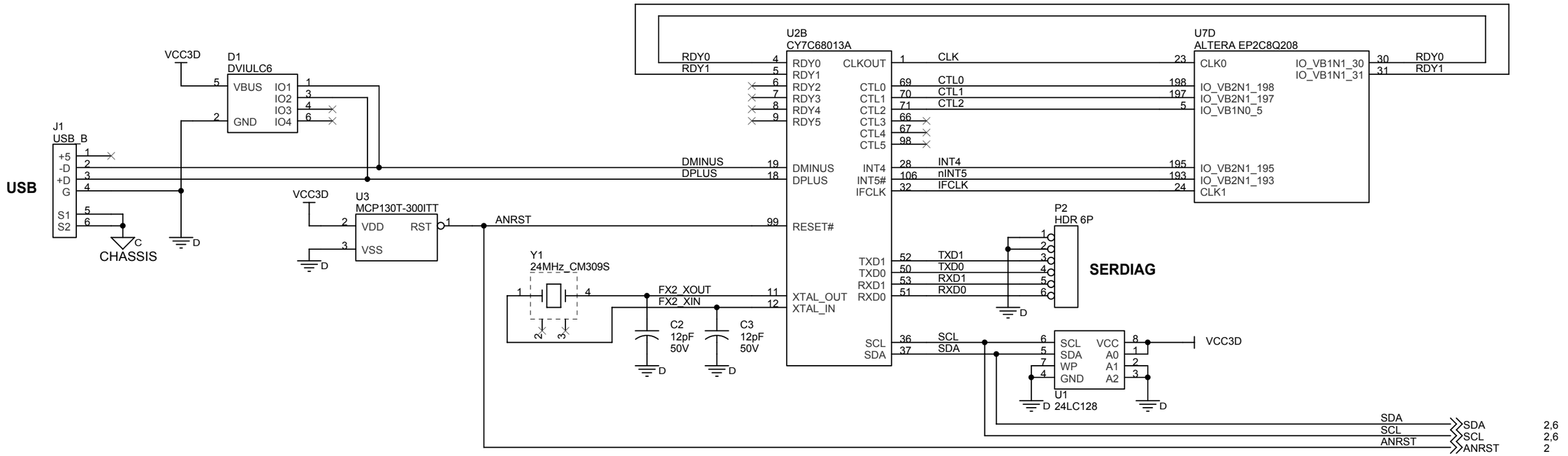

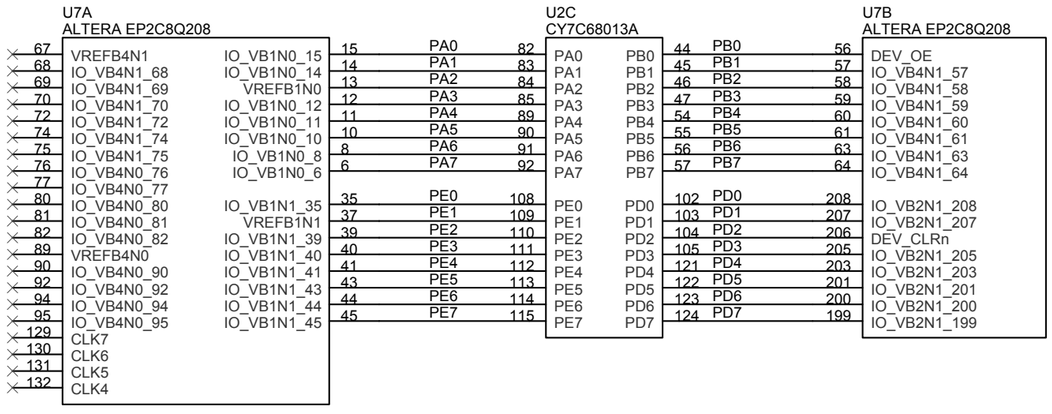

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Size B	Document Number 2009071901		Rev A
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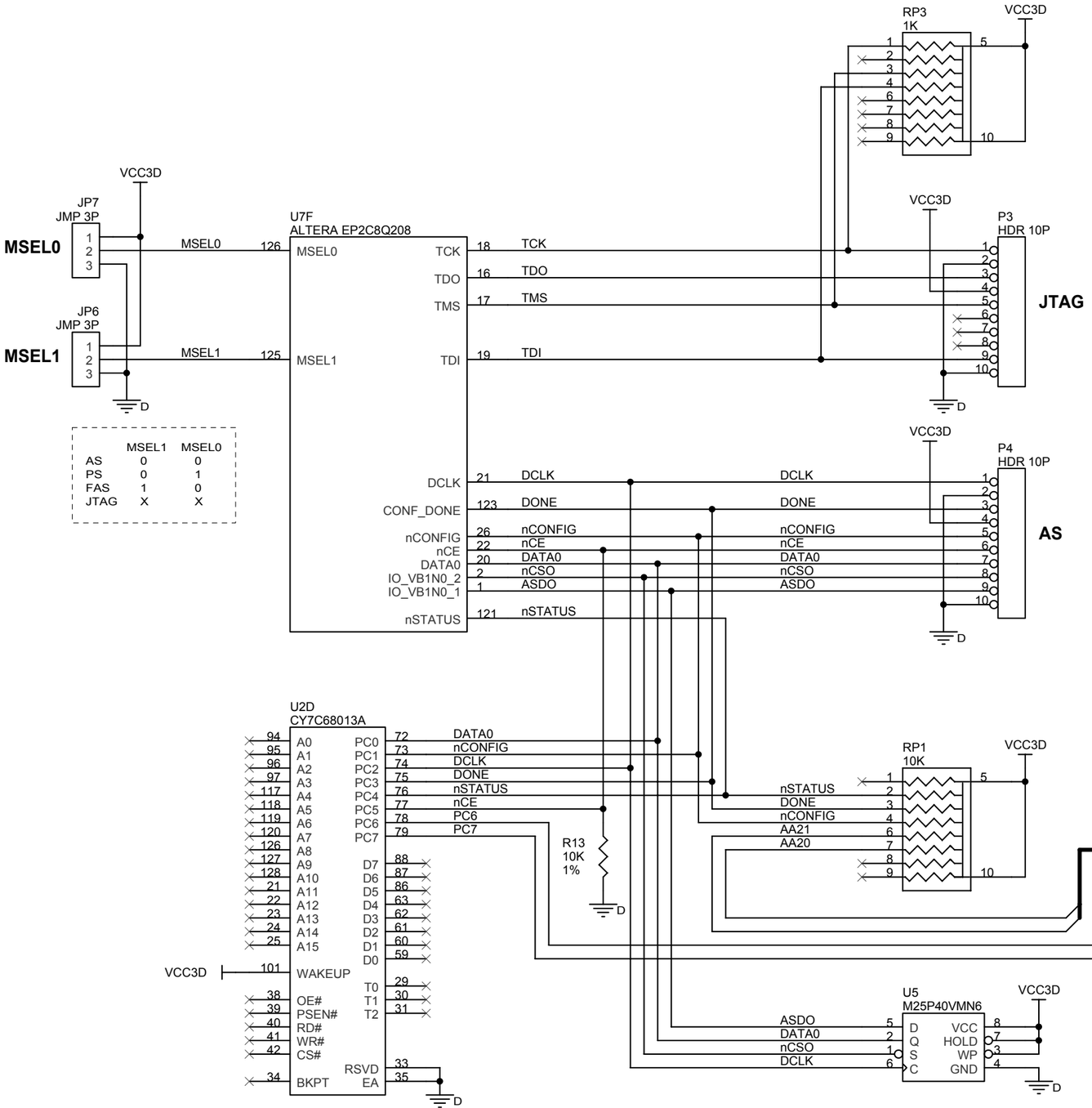
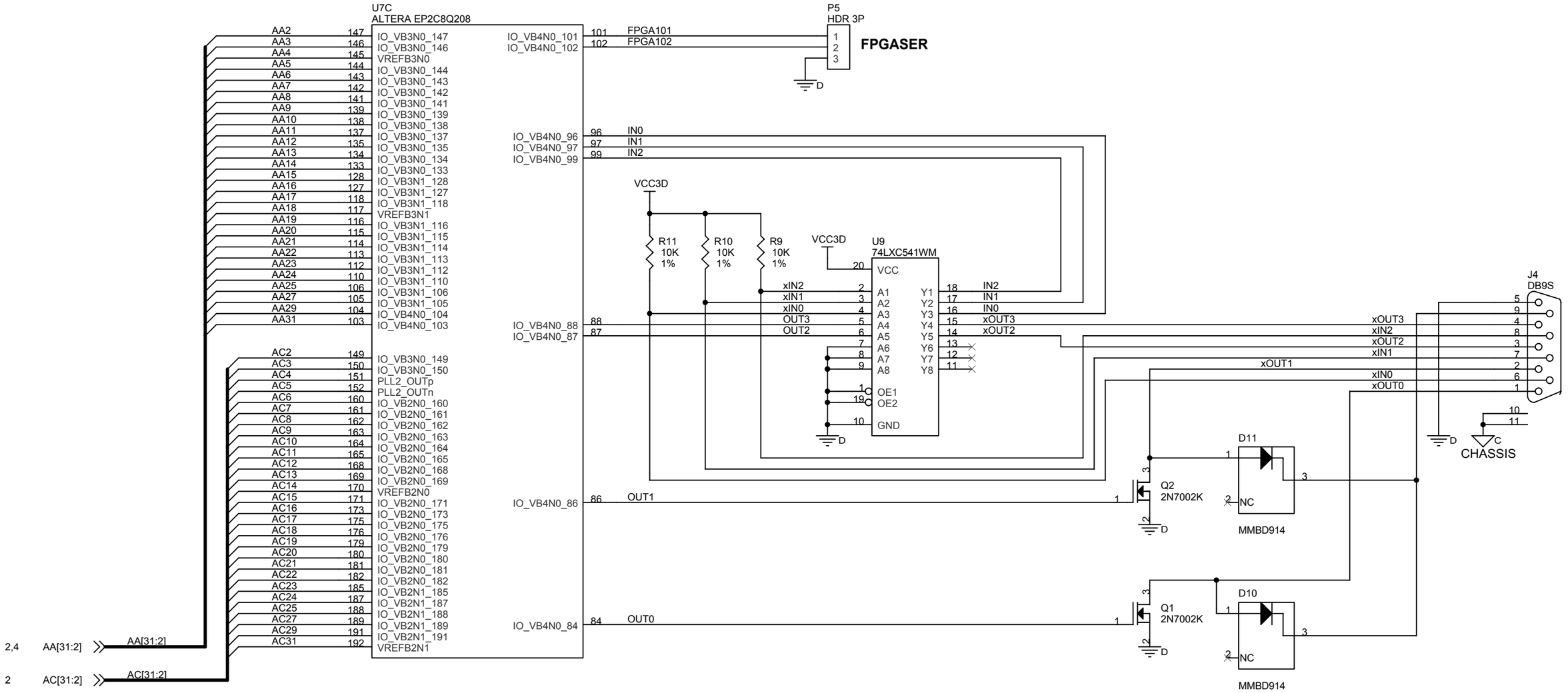


Title	HPSDR Magister: FPGA Configuration		
Size B	Document Number 2009071901		Rev A
Date: Sunday, September 19, 2010	Sheet 4 of 6		



Title	HPSDR Magister: FPGA	
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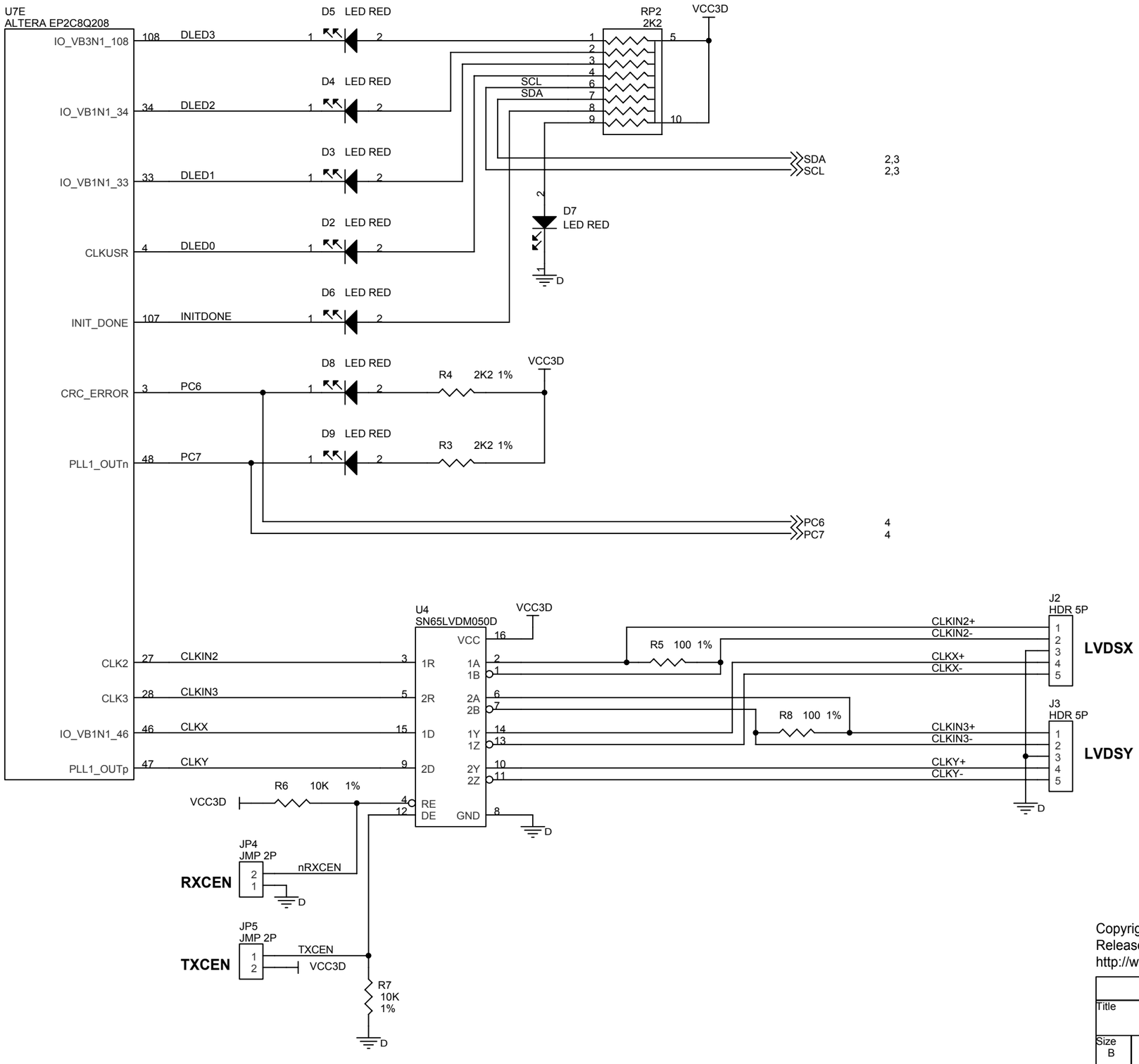

Title	HPSDR Magister: LEDs and I/O	
Size B	Document Number 2009071901	Rev A
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